L Number	Hits	Search Text	DB	Time stamp
-	0	(716/10).ccls. and (crossbar adj device)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 10:09
-	3	(716/$).ccls. and (crossbar adj device)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 11:30
-	91	(716/$).ccls. and (logic$ adj interconnect$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/20 17:52
-	0	(716/$).ccls. and (logic$ adj interconnect$) adj ((pass or control) adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/20 17:54
-	15	dimyan near1 magid	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 09:56
-	5226	cross adj connect	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 11:22
-	2	(716/$).ccls. and (cross adj connect)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 11:23
-	55	(716/$).ccls. and (crossbar)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 12:50
-	538	(716/$).ccls. and multiplexer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 12:50
-	168	(716/$).ccls. and (multiplexer and decoder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 13:04
-	21	crossbar and decoder and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 15:40
-	21	(crossbar or crossconnect) and decoder and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 15:43
-	0	crossconnect and decoder and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 15:43

-	1516	decoder and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 16:47
-	25	(716/$).ccls. and decoder and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 16:08
-	2	(multiplexer adj type) adj crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 16:29
-	0	partial adj (crossbar adj connect$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 16:36
-	51	decoder and (pass adj transistor) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:24
-	0	decoder and ((first adj pass) adj transistor) and ((second adj pass) adj transistor) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:26
-	0	((first adj pass) adj transistor) and ((second adj pass) adj transistor) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:28
-	30	(pass adj transistor) and chain and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:46
-	5585	((pass adj transistor) or switch) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:48
-	0	(716/$).ccls. and (cascade$ and (pass adj transistor)) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/21 18:55
-	0	(716/10).ccls. and latch and (crossbar adj device)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 10:08
-	6	latch and (crossbar adj device)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 10:12
-	106	latch and mutiplex$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 10:51

-	1	(716/$).ccls. and (latch and mutiplex$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 11:04
-	148	(716/$).ccls. and (latch and (input adj data))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 11:21
-	6	(716/$).ccls. and ((input adj data) with switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 11:36
-	1	(716/$).ccls. and ((input adj data) adj switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 11:49
-	3425	(716/$).ccls. and (input data switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 11:51
-	1	(716/$).ccls. and "input data switch"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 12:00
-	241	"input data switch"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 12:02
-	5	"input data switch" and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 12:16
-	0	"input data" with "pass adj transistor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 12:17
-	897	(input adj data) and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 12:19
-	26	(716/$).ccls. and (input adj data) and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 13:58
-	1	"4698760".PN.	USPAT	2002/05/22 12:48
-	36	(716/$).ccls. and (input adj buffer) and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 14:44
-	487	(input adj buffer) and (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 14:56

-	5	memory and ((low adj power) adj structure)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 15:09
-	1895	vth and buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 15:09
-	1	(power adj on) adj reset	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 15:53
-	5828	POR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 15:53
-	207	POR and (output adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 16:07
-	11	POR with (output adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:51
-	2	POR and (output adj buffer) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 16:29
-	0	((power adj off) adj reset) and (output adj buffer) and crossbar	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/22 16:31
-	0	(716/$).ccls. and (pass adj transistor) with (input adj fuffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 10:11
-	34	(pass adj transistor) with (input adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 10:39
-	70	input adj (pass adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 10:52
-	21	input adj (pass adj transistor) and (mux or multiplexer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 10:54
-	0	((power adj on) adj reset) with (flip adj flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:20

-	1	((power adj on) adj reset) and (flip adj flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:29
-	1	((power adj on) adj reset)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:23
-	0	((power adj on) adj reset) and (output adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:31
-	1209	(pass adj transistor) and (threshold adj voltage) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 11:55
-	961	(pass adj transistor) and (threshold adj voltage) and memory and (input and output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 12:12
-	3	("5282271").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 12:08
-	18	(716/$).ccls. and (pass adj transistor) and (threshold adj voltage) and memory and (input and output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 12:24
-	2	(716/$).ccls. and (pass adj transistor) and (threshold adj voltage) and memory and (input adj voltage) and (output adj voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/05/23 12:26